



Pravara Rural Education Society's
Pravara Rural Engineering College, Loni
Department of Electronics & Telecomm. Engg.



Time Table
Semester-I (Academic Year 2018-2019)w.e.f. 16/06/2018

Hall No.09

B.E. (E&TC)



Time / Day	9.00 to 10.00	10.00 to 11.00	11.00 to 11.15	11.15 to 12.15	12.15 to 1.15	1.15 to 2.15	2.15 to 4.15	
	1	2	R E C E S S	3	4	L U N C H	5	6
Monday	EPD	CNS		RMT	VLSI		RMT(C) EL-I(A) VLSI(D)	
Tuesday	GATE			GATE			GATE	
Wednesday	RMT	VLSI		EL-I	EPD		RMT(A) CNS(B) VLSI(C) EL-I(D)	
Thursday	CNS	EL-I		EPD	RMT		RMT(B) EL-I(C) CNS(D)	
Friday	VLSI	RMT		EL-I	CNS		VLSI(A) CNS(C) RMT(D)	
Saturday	CNS(A) EL-I(B) VLSI(B)			CNS(A) EL-I(B) VLSI(B)			Project	

Sr.No	Subject	Name of Faculty	Batches with Roll No
1.	VLSI (Th&Pr D)	Prof.S.M.Turkane	A 1-20 B 21-40 C 41-60 D 61Onwards
2.	VLSI (PrA,B,C)	Prof.D.A.Ansari(C.T.)	
3.	EL-II (Th) -Elect. Product Design(EPD)	Dr.S.G.Galande	
4.	CNS (Th&Pr)	Prof.S.B.Mandlik	
5.	RMT(Th&Pr)	Prof.V.A.Aher	
6.	EL-I (Th&Pr) Embedded System	Prof. M.S.Anap	

Prof.S.B.Mandlik
Time-Table I/C

Prof.S.G.Galande
HOD(E&TC)

Dr. Y.R. Kharde
Principal

	Pravara Rural Education Society's Pravara Rural Engineering College, Loni Department of Electronics & Telecomm. Engg.	
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Time Table
Semester-I (Academic Year 2018-2019)w.e.f. 16/06/2018

Hall No.01

S.E. (E&TC)

Time / Day	9.00 to 11.00	11.00 to 11.15	11.15 to 12.15	12.15 to 1.15	1.15 to 2.15	2.15 to 3.15	3.15 to 4.15
	1	2	3	4		5	6
Monday	EDC(A) ECM(B)DE(C) EMIT(D)		EDC	SS	L U N C H	DSA	DE
Tuesday	DE(A) DSA(B) ECM(D) SS(C)		ECM	DSA		EDC	DE
Wednesday	EDC(C) DSA(A) EMIT(B) SS(D)		ECM	DE		EMIT	EDC
Thursday	EDC(D) ECM(A) DE(B) DSA(C)		EDC	DE		SS	DSA
Friday	EDC(B) ECM(C) EMIT(A) DSA(D)		SS	DSA		ECM	SS
Saturday	SS(A,B)DE(D)		EMIT(C)DE(D)			-	



*SS(A,B)-Hall No 9 & SS(C,D) Power Lab

Sr.No	Subject	Name of Faculty	Batches with Roll No
1.	DE (Th)	Prof.S.B.Mandlik	A 1-20
2.	DE (Pr)	Prof.R.N.Kadu	
3.	ECM (Th&Pr)	Prof.P.C.Warule(C.T.)	
4.	DSA(Pr D)	Dr.S.G.Galande	B 21-40
5.	DSA (Th&PrA,B,C)	Prof.D.A.Ansari	C 41-60
6.	EDC (Th&Pr)	Prof. S. S. Turkane	
7.	EMIT (Th&PrA,C)	Prof.B.A.Pangavhane	D 61 Onwards
8.	EMIT (PrB,D)	Prof. M.S.Anap	
9.	SS(Th&Tut)	Prof.B.A.Pangavhane	

Prof.S.B.Mandlik
Time-Table I/C

Prof.S.G.Galande
HOD(E&TC)

Dr. Y.R. Kharde
Principal

	Pravara Rural Education Society's Pravara Rural Engineering College, Loni Department of Electronics & Telecomm.Engg.	
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Time Table
Semester-I (Academic Year 2018-2019)w.e.f. 16/06/2018

Lect No 1, 2: Hall No.01
Lect No 5, 6: Hall No.09

T.E. (E&TC)

Time / Day	9.00 to 10.00	10.00 to 11.00	11.00 to 11.15	11.15 to 1.15	1.15 to 2.15	2.15 to 3.15	3.15 to 4.15
	1	2	R E C E S S	4	L U N C H	5	6
Monday	ESD	DSP		ESD(B) MT(C) M&A(D) DSP(A)		MT	DC
Tuesday	MT	DC		MT(A) DC(C) DSP(D) M&A(B)		DSP	M&A
Wednesday	M&A	DSP		M&A(A) DC(B) DSP(C) MT(D)		EMT	DC
Thursday	DSP	DC		DSP(B) ESD(C) DC(D) EMT(A)		EMT	EMT(B)
Friday	M&A	EMT		ESD(D) DC(A) MT(B)M&A(C)		MT	ESD
Saturday	EMT(C) ESD(A)			EMT(D) ESD(A)		-	-



EMT Tutorial batch will be conducted in MP Lab

Sr.No	Subject	Name of Faculty	
1.	DC (Th&PrA)	Prof.A.H.Ansari	Batches with Roll No A 1-20 B 21-40 C 41-60 D 61 Onwards
2.	DC (PrB,C,D)	Prof.B.A.Pangavhane	
3.	ESD(Th)	Prof. S.R. Aher	
4.	ESD (Pr)	Prof.P.C.Warule	
5.	EMT (Th& Tut)	Prof. S.R. Aher(C.T.)	
6.	DSP(Th&Pr)	Prof. R.A. Kadu	
7.	MT (Th&Pr D)	Prof. S. S. Turkane	
8.	MT(PrA,B,C)	Prof. S.R. Aher	
9.	M& A (Th&Pr)	Prof.R.N.Kadu	

Prof.S.B.Mandlik
Time-Table I/C

Prof.S.G.Galande
HOD(E&TC)

Dr. Y.R. Kharde
Principal

	Pravara Rural Education Society's Pravara Rural Engineering College, Loni Department of Electronics & Telecommunication Engineering	
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Hall No. 02

Time Table — Semester-I

(Academic Year 2018-2019)

M. E. Second Year (E &TC)(VLSI& Embedded System)w.e.f.02/07/2018

Time / Day	9.00 to 10.00	10.00 to 11.00	11.00 to 11.15	11.15 to 12.15	12.15 to 1.15	1.15 to 2.15	2.15 to 3.15	3.15 to 04.15
	1	2		3	4		5	6
Monday	EL- III	TVC	R E C E S S	ASIC	ASIC	R E C E S S	ASIC	TVC
Tuesday	TVC	EL- III		TVC	ASIC		EL-III	EL-III
Wednesday	EL- III	Seminar II		Seminar II			Seminar II	
Thursday	Seminar II			Seminar II			Seminar II	
Friday	-			-			-	
Saturday	-			-			-	

EL-III (Wed) lectures will be conducted in MWELab

Sr. No.	Subject	Name of Faculty
1.	Elective –III	Prof.S.M.Turkane
2.	TVC (Testing & Verification of VLSI Circuits)	Prof.S.G.Galande
3.	ASIC Design	Prof. S.B.Mandlik
4.	Seminar-II	All Guides

Prof.S.B.Mandlik
Time-Table I/C

Prof.S.G.Galande
HOD& M.E.Coordinator

Dr. Y.R. Kharde
Principal